

On page 10, line 14, change "co-planar" to ~~co-planar~~--; and

On page 10, line 17, change "material remaining 42 and" to ~~material~~ 42 remaining--.

IN THE CLAIMS:

In claim 1, line 5, before "layers" insert --trace--;

In claim 1, line 6, delete "conductive";

In claim 2, line 1, delete "conductive";

In claim 7, line 2, change "is" to --comprises--;

In claim 9, line 2, delete "conductive";

In claim 10, line 2, after "the" insert --at least one--;

In claim 10, line 3, after "portion" and before the comma, insert --thereof--;

In claim 11, line 1, after "the" at the end of the line, insert --at least one--;

In claim 13, line 3, after "the" insert --at least one--;

In claim 13, line 6, after "and" insert --the--;

In claim 13, line 7, after "by" insert --the--; and

In claim 14, line 2, delete "at least one of".

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15. (amended) A printed circuit board comprising:

*A*  
at least one voltage reference plane substantially coextensive with a portion of a substrate; and  
at least one signal trace substantially [co-planar] coplanar with the at least one voltage reference plane and electrically isolated therefrom, the at least one voltage reference plane having a substantially greater surface area than any one signal trace.

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*AZ*  
17. (amended) The printed circuit board of claim 15, further comprising a passivation layer deposited on the at least one signal trace and the at least one [electrically insulative layer] voltage reference plane.

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